

**INDIAN INSTITUTE OF ENGINEERING SCIENCE AND
TECHNOLOGY, SHIBPUR**



BIDDING DOCUMENT

(Project Code: DRC/DST-WB (STB) /CST/SG/024/18-19)

Ref. Advt. No. CS 1578, published in "The Statesman (All Editions)" & "Dainik Statesman", dated 17.01.2020.

For Supply of

***Xilinx Kintex-7™ FPGA
(XC7K325T-2FFG900C) based board***

Under

**Department of Science & Technology and Bio-technology,
Govt. of West Bengal**

Department of Computer Science and Technology

January 14, 2020

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

(Institute Project Code: DRC/DBT/CST/SG/004/17-18)

Department of Computer Science and Technology
**Indian Institute of Engineering Science and Technology (IEST) Shibpur
P.O. Botanic Garden, Howrah – 711 103, West Bengal, India**

Complete sealed quotations are invited for the supply of items listed below as per specification mentioned. The quotation should include the delivery charges of the items to Indian Institute of Engineering Science & Technology, Shibpur, and should mention a firm delivery period.

1. Important Information:	
Location of supply:	Computer Science and Technology Indian Institute of Engineering Science and Technology Shibpur, P.O. Botanic Garden, Howrah 711 103
Name of Work:	Supply and Installation of Two Xilinx Kintex-7™ FPGA (XC7K325T-2FFG900C) based board
Estimated value of the equipment	₹ 1.30 Lakhs
Estimated time of supply:	Within two months of placement of order
Earnest Money Deposit	2% of the estimated cost in favour of “The Registrar, IEST Shibpur” payable at Kolkata.
Date of publishing tender	17.01.2020
Bids shall be submitted to:	Bidders are to submit the quotations in Sealed Cover to the Department of Computer Science and Technology in the following address: Dr. Surajeet Ghosh Department of Computer Science and Technology Indian Institute of Engineering Science and Technology (IEST) Shibpur, P.O. Botanic Garden, Howrah – 711 103, West Bengal, India
Last date of submission of bid	10 working days from the date of advertisement (up to 3.00 PM IST)
Date of Opening of Bid	Next working day at 02.00 PM IST
Validity	90 days from the date of opening.

2. Technical bid shall consist of the following Information and supporting documents towards:	
1	Name of the firm
2	Address of the Registered Office
3	Contact Phone No/ Contact E-mail address
4	Constitution or legal status of Bidder
5	Place of registration
6	Principal place of business
7	Power of attorney of signatory
8	Valid Trade License
9	PAN and VAT
10	Authorization letter (in case of dealer)
11	Earnest Money Deposit
12	IT clearance certificate for last three years
13	ISO 9001:2008 for quoted item.
14	Bidder quotation no. and date

(A. Code DRC-T063/19-20)

Dean (R & C)

3. Terms and conditions:

- i) All rates quoted should be **inclusive** of all taxes, levies, duties, transportation, Packing, forwarding and Insurance Charges (if any). The rates should be quoted both in figures and words. **Request for inclusion of any tax/levy at later stage will not be entertained.**
- ii) **The Institute reserves the right to accept or reject all or any of the tenders without assigning any reason whatsoever. The decision of the Institute shall be final in case of any dispute.**
- iii) **The vender must complete the work within 60 days from the date of receiving the order.**
- iv) The EMD of the successful vendor will be converted as performance security and to be refunded after satisfactory completion of the installation.
- v) Materials & accessories used should be as per specification and of Approved Quality (B.I.S.) or by the authorized officer of I.I.E.S.T. Shibpur.
- vi) Bidder shall furnish an undertaking for providing Comprehensive onsite warranty for any manufacturing defect for a period of three years against each furnished item from the date of supply/installation on their letterhead, failing which, their offer will not be considered.
- vii) Bills, Challans in Triplicate & installation report should be presented for payment within 15 days of supply/completion of work.
- viii) The Order no. is to be noted on both challan and Bill.
- ix) All bills are to be accompanied by order copies and challans as received.
- x) Payment will be made within 60 days of Submission of proper bills; challans etc. by A/c payee cheque or NEFT, no cash payment will be made under any circumstances.
- xi) Company's Bank account no. with IFSC code, Scanned copy of PAN Card, Certificate of VAT will be attached with e-Tender technical cover.
- xii) All payments are subjected to statutory deductions as and when applicable.
- xiii) Scanned copy of the latest Income Tax, Sales Tax, Professional Tax clearance certificate, Trade License.
- xiv) Bidder shall furnish a non-blacklisting affidavit on non-judicial stamp paper of Rs.10/- duly attested by the Notary that there is no vigilance/CBI case pending against the firm/supplier and the firm has not been blacklisted in the past by any government/Private institution.
- xv) **SELECTION WILL BE MADE PURELY ON ESSENTIAL TECHNICAL DOCUMENTS.** The IIESTS authority will evaluate and compare the quotations (the gross amount i.e. inclusive of all taxes, other charges etc.) determined to be substantially responsive i.e. which- (a) meet the qualification criteria specified in the tender, (b) Are properly signed, and (C) Conform to the terms and conditions, specifications without deviations.
- xvi) Only one best quality item (according to our specification) should be quoted against each item. On no account should different qualities e.g. A, B, & C of items be quoted. Those items that have been quoted as per different qualities (In contravention to the specifications) will not be considered at all and will be summarily rejected.
- xvii) Based on our specifications, image/drawing of item to be provided and Model Name/No. (if applicable) and Brand must be clearly mentioned in the quotation, without which quotation will not be considered.
- xviii) In case of non supply of material within the due date i.e. within the date of delivery, the Director, IIEST Shibpur will have the right to impose penalty like forfeiture of performance security and removal of the name from the list of the vendor and resort to risk purchase in full or part thereof at his/her discretion, his/her decision shall be final and bind.

- xix) Original Manufacturer/firm must have manufacturing facility certified by ISO 9001:2008 for quoted item. Certificates to be attached with technical bid, without which their quotation will not be considered.
- xx) **In case, the firm quoting for above mentioned items is the Authorized Dealer / Distributor of the Manufacturer. Copy of authorization certificate should be enclosed with the quotation. Offers Submitted without proper authorization shall be rejected summarily.**
- xxi) The supplier must have submitted the catalogue of the product along with the technical bid. The supplier must also provide a list of premier institutions (Like IITs/ NITs or Internationally reputed Universities, etc.) to which similar product has been supplied with product details. A compliance certificate to be provided item wise as per the specification.

We accept the above terms and conditions.

Dated:

Signature of Bidders/Suppliers

With date & Seal

4. TECHNICAL SPECIFICATIONS:

Xilinx Kintex-7™ FPGA (XC7K325T-2FFG900C) based board

Quantity - 2

S.L. No.	Specification of the equipment	Qty.	Unit Rate * (in Rs)	Amount (in Rs)
1	<p>Xilinx Kintex-7™ FPGA (XC7K325T-2FFG900C) based Board: 50,950 logic slices (up 7x), each with four 6-input LUTs and 8 flip-flops Close to 16 Mbits of fast block RAM (up 7x) Ten clock management tiles, each with phase-locked loop (PLL) 840 DSP slices (up 17x) Internal clock speeds exceeding 450MHz On-chip analog-to-digital converter (XADC) Up to 10.3125Gbps gigabit transceivers 1800Mbps DDR3 data rate with 32-bit data width Commercial -2 speed grade Fully-populated 400-pin FMC HPC connector w/ ten GTX lanes USB-UART Bridge, 128x32 pixel OLED 16-bit VGA connector, Pmod connector for XADC signals Two four-lane Display Port connectors HDMI Sink and HDMI Source 10/100/1000 Ethernet PHY 1GB 1800Mbps on-board DDR3 USB 2.0 Host/Device/OTG PHY Dedicated USB port for JTAG programming and data transfers Micro SD card connector, Audio codec w/ four 3.5mm jacks Serial Flash, Five Pmod ports USB HID Host for mice, keyboards and USB MSD Host for storage</p>	2		
Total Quoted Amount (in Rs.)				

* Inclusive of all taxes, levies, duties, transportation, Packing, forwarding and Insurance Charges (if any).